



DECLARATION OF INVENTORS

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As a below named inventor, we declare that:

Our residence, post office addresses and citizenships are as stated below next to our names; that we believe we are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention or design entitled Title of Invention: SYSTEM AND METHOD FOR DETECTING QUIESCENT CURRENT IN AN INTEGRATED CIRCUIT, the specification of which (check one):

	_is att	ached	her	eto; or	•					
<u>X</u>	_was	filed	on	Nov.	30,	2001	as	Application	Serial	No
09/9	97,78	6 and	was	s amer	nded	on		(if applical	ble);	

that we have each reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above; and that acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to me to be material to patentability as defined in 37 C.F.R. § 1.56.

We hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application(s) in the manner provided by the first paragraph of 35 U.S.C. § 112, we acknowledge the duty to disclose to the U.S. Patent and Trademark Office all information known to us to be material to patentability as defined in 37 C.F.R. § 1.56 which became available between the filing date of the prior application(s) and the national or PCT international filing date of this application:

Application		
Serial Number	_Date_	Status
None	None	None

I hereby claim the benefit under Title 35, United States Code, § 119(e) of any United States provisional application(s) listed below:

60/250,735 December 1, 2000 (Filing Date)

We declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United

States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

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Full name of the first inventor

Inventor's signature

Date

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Full name of additional joint inventor, if any

Inventor's signature

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